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Efficient Microstrip Antenna system using Reduced GP Structure for Ultra-Wide Band (UWB) Applications

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Abstract: Microstrip antenna is an essential choice for Ultra Wide Band (UWB) applications of its light weight, low profile and easy to form antenna arrays. However, the design of microstrip patch antenna bandwidth is greatly affects by the dielectric substrate material (FR4). In this research, the bandwidth enhancement of MPA was designed by minimizing the dimension of Defected GP (DGP) in GP for Ultra Wide Band wireless applications. But, the antenna design complexity increases with the number of an operating frequency band. In this research, the MPA was designed as small as size of 10×13×1.6 mm and operates on frequency band between 3.1GHz to 10.6GHz for VSWR less than 2. The microstrip patch antenna was designed at 3.1GHz to 10.6GHz using High-Frequency Structure Simulator (HFSS) software. The simulation result shows that the proposed microstrip patch antenna obtained <-10dB of return loss from 3.1GHz to 10.6GHz throughout the frequency range. The measured result proves that the proposed microstrip patch antenna has better characteristics to fulfill the requirements of UWB applications.

Keywords: *Bandwidth, Ground Plane (GP), Microstrip patch antenna (MPA), Operating frequency, Spectrum, and Ultra Wide Band, (WCS) Wireless communication systems.*

1. Introduction

Recently, the WCS require high quality, cost-effective and Miniaturized Antenna Devices (MAD) with enhanced features because of an increase in data rates and trend of miniature electronics circuits. The MPA is widely used for WCS because of their lightweight, low profile and cost-effective [1]. The MPA contains a radiating patch on one of a dielectric substrate, which has a GP on another side. Generally, the microstrip patch is made of conducting material like copper and it can take any effective possible shape. The radiating microstrip patch and the feed lines are usually photo etched on the dielectric substrate. [2]. The design of microstrip antennas has benefited from the unrelenting growth in computational power and the increased availability of accurate and efficient methods to numerically solve Maxwell's equations [3]. The microstrip patch antennas suffer from a number of limitations as patch length is around half a wavelength [4]. The performance of the microstrip antenna can be developed in terms of gain, matching performance, impedance bandwidth, and return loss by presenting a defect in the GP [5]. F The DGS is either non-periodic or fixed periodic cascade configuration defect in GP and any type of defect in the GP can give rise to increase in effective inductance and capacitance [6]-[7].

The GP is one of the important parts of a microstrip patch antenna, since it affects several of the antenna's characteristics. Therefore, it is necessary for an antenna designer to understand the effects of the GP on antenna characteristics in order to be able to select a GP size suitable for the particular application [8]. Numerous approaches have been utilized to achieve compact microstrip antenna (for example high dielectric constant substrate, adopting short-circuit pin, and slots loaded on the patch) [9]. The traditional antenna design scheme requires a complex structure and feeding technique like more layers and parasitic structures. To overcome the complexity of antenna structure, this research the bandwidth enhancement of MLF is designed by minimizing the length of DGP in GP for UWB wireless applications. The major aim of the proposed microstrip patch antenna is obtaining wide broad bandwidth with high radiation efficiency and decrease the size of GP structure. Several factors must be taken into account including impedance, physical profile, bandwidth, radiation efficiency, radiation pattern, return loss, and VSWR to select optimized antenna topology for UWB applications by using HFSS software.

This research paper is composed as follows, Section 2 presents an extensive survey of recent papers on GP structure reduction techniques for microstrip antennas. Section 3 briefly described the proposed microstrip patch antenna design. Section 4 presents the experimental result of proposed microstrip antenna and the obtained results are compared with existing antenna designs. The conclusion is made in Section 5.

2. Literature Survey

The researchers have suggested a number of GP structure reduction techniques for microstrip antennas. Brief evaluations of a few significant size reduction techniques for microstrip antenna are presented in this section.

Jain and Shubhi [10] designed Planar Microstrip Patch Antenna (PMPA) with a full GP, which is suitable to operate at 5.5 GHz frequency with 200 MHz bands. The return loss at 5.5 GHz is -29.5dB for inset feed antenna. The maximum gain was 5.7 dB and bandwidth was 200 MHz for entire ground and 210 MHz for the defected GP antenna. This proposed antenna used for the Wireless Land Area Network (WLAN) applications. The return loss of -36.81dB was improved after introducing a pyramidal shape defected GP at the same frequency. The width of the patch and length is high in the proposed method.

Pandhare *et al.* [11] proposed work was obtained a miniaturized Microstrip Patch Antenna Array (MPAA) utilizing DGS for S-band at 2.2 GHz. First, the patch antenna array was designed at C-band resonates at 5.2 GHz of frequency. The Proposed DGS was integrated into GP of patch antenna array used for size reduction. In this work, the proposed DGS was integrated in the GP of the patch antenna array for antenna size reduction. By using this way, to enhance the gain of proposed miniaturization radiator and the patch radiator was modified to retain its properties of radiations. At the final stage, the resonance frequency of original MPAA shifts from 5.2 GHz to 2.2 GHz and with good performance reduction up to 83 %. The proposed method is obtained -16dB S₁₁-parameter, it is not sufficient for effective antenna applications.

Matekovits *et al.* [12] proposed a mutual coupling reduction between Implanted Microstrip Antennas (IMAs) on cylindrical bimetallic GP. In this work cylindrical bio-metal implant serves as the common ground plane for the conformal antennas. This cylindrical bio metal serves as the common GP for conformal antennas. Then the mutual coupling between two- conformal microstrip antennas was studied and quantified for various spacing between the antennas. Three methods were proposed such as top method, middle method and bottom method to minimize mutual coupling between two - antennas. But, maximum coupling reduction is 7 dB at 75 degree for small angles and the mutual coupling reduction is not significant.

Wei *et al.* [13] proposed S-shaped periodic DGS to reduce mutual coupling between the antenna elements. This proposed PDGS method significantly disturbs the fields and induces currents in microstrip antenna elements to reduce mutual coupling. The periodic DGS method obtained more than 40 dB mutual coupling reduction in microstrip antenna elements. Furthermore, there is no much significant variance between

the simulated and measured Main Lobe Patterns (MLP) in upper sphere space. But, the size of the proposed antenna is more complex.

Painam, Surendrakumar, and Chandramohan Bhuma [14] proposed a size reduction technique when a circular microstrip patch was loaded with a meta-material structure. This technique utilized a complementary split ring resonator with partially loaded Non-Uniform Meta Surface (NUMSs) on the GP for obtaining better performance. The proposed antenna resonating at 6.22 GHz was designed, fabricated, tested and measured. The proposed antenna was obtained 74 % of size reduction compared to traditional microstrip antenna. The proposed antenna can be utilized for wireless applications like indoor base station antenna for vehicle-to-vehicle communications. The substrate dimension is high $23 \times 24 \times 0.51$ of antenna.

3. Antenna Design and Performances

The proposed microstrip patch antenna design consists of a monopole antenna with a microstrip feed line and the GP. The proposed antenna design is used to attain multiband just through placing the reduced dimensions of DGS in GP, so it much easier to fabricate. In this research, the proposed microstrip slot antenna is developed for UWB applications. The UWB technology has regarded as wide technologies in the wireless world due to their efficient features.

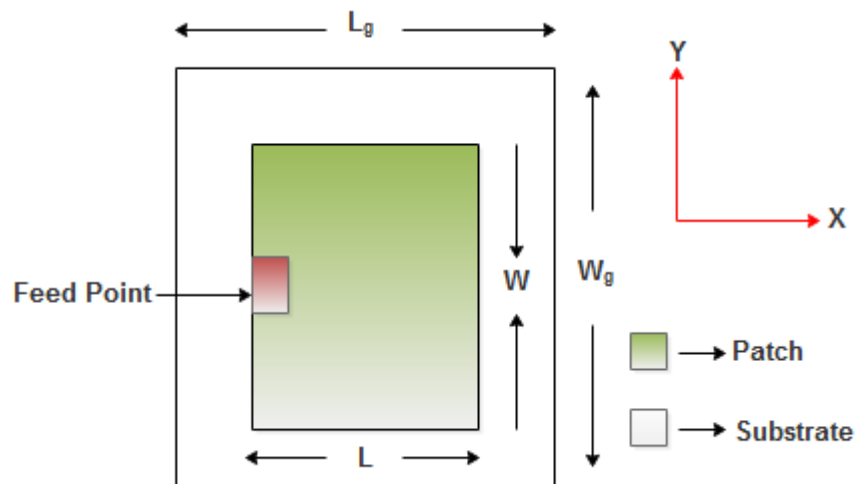


Figure. 1 Proposed microstrip antenna design

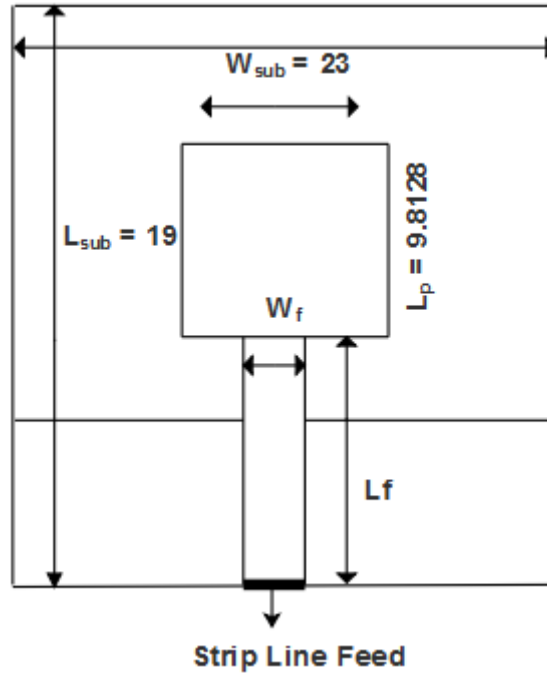


Figure. 2 Geometry of the front view for the proposed microstrip antenna

The proposed microstrip patch antenna is shown in fig. 1. Fig. 2 depicts geometry of the front view for the proposed microstrip antenna at the edge of the patch. In this research, width and the length of the patch will be affecting the performance of the patch antenna. Hence some important parameter such as bandwidth, impedance, return loss, and VSWR are computed by the formulas to achieve a compact structure of the microstrip antenna. The proposed microstrip patch antenna consists of substrate, GP and patches. The size of the proposed antenna is $13 \times 10 \times 1.6$ mm.

3.1 Design of microstrip antenna at 6.85 GHz

The configuration of the simulated microstrip antenna is designed and fabricated on a substrate with FR4_epoxy, the relative permittivity of 4.4, and a loss tangent of 0.02. In this proposed antenna design, the entire size of the antenna is only $10 \times 13 \times 1.6$ mm. The microstrip antenna should be operated in 3.1GHz to 10.6GHz of the frequency range. So, the width of the patch is chosen from eq. (1). By using these computations, the dimensions of the antenna are concluded in Tab .1. The simulation results of the fabricated microstrip antenna is shown fig 3 & 4.

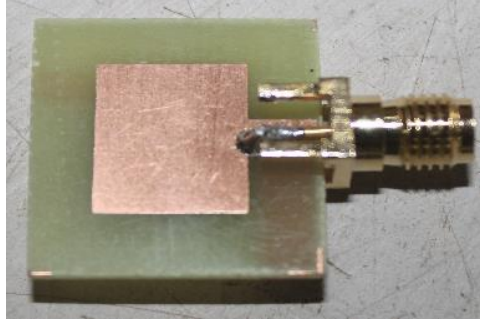


Figure. 3 Photography of top view fabricated microstrip antenna



Figure. 4 Photography of back view fabricated microstrip antenna

From the numerical computational methods, the simulation studies and exhaustive experimental following design equation are derived for reduced the GP structure, which allows to compute the length of a patch at 6.85 GHz of operating frequency. The width of patch is computed by eq. (1).

3.1.1 Computation of the width of patch

The width is critical in impedance and power efficiency, it depends upon the operating frequency and height of the substrate.

$$W = \frac{c}{2f_0} \sqrt{\frac{2}{\epsilon_r + 1}} \quad (1)$$

Here, c is speed of light (3×10^8 m/s), ϵ_r is permittivity of substrate (4.4), f_0 is resonance frequency (6.85 GHz). By using equation (1) the width of patch is 13.33mm.

3.1.2 Calculation of effective dielectric constant

Effective dielectric constant is minimum than dielectric constant due to fringing field around the margin of the patch.

$$\epsilon_{eff} = \frac{\epsilon_r + 1}{2} + \frac{\epsilon_r - 1}{2} \left[1 + 12 \frac{h}{w} \right]^{-\frac{1}{2}} \quad (2)$$

Here, h is the height of substrate (1.6 mm), w is width of the patch (13.33 mm) and ϵ_r is 4.4. By using equation (2) the effective dielectric constant value is 3.788.

3.1.3 Calculation of effective length

The transmission line method is applicable to infinite GP. However, it is important to have a finite GP for practical consideration. The calculation of effective length has been exposed by the similar result for infinite and finite GP. The effective of the length is expressed in equation (3).

$$L_{eff} = \frac{c}{2f_0\sqrt{\epsilon_{eff}}} \quad (3)$$

Here, ϵ_{eff} is 3.788, c is 3×10^8 m/s and f_0 is 6.85 GHz. The effective length is 11.25.

3.1.4 Calculation of length extension and length of patch

The practical approximations used for length extension (ΔL) is expressed by using equation (4).

$$\Delta L = 0.412 \times h \frac{(\epsilon_{eff} + 0.3) \times (\frac{w}{h} + 0.264)}{(\epsilon_{eff} - 0.258) \times (\frac{w}{h} + 0.264)} \quad (4)$$

Here, w is 13.33 mm, h is 1.6 mm and ϵ_{eff} is 3.788. The length of extension is 0.843.

The actual length of the patch is expressed by equation (5).

$$L = L_{eff} - 2\Delta L \quad (5)$$

Here, L_{eff} is 11.25 mm and ΔL is 0.843. The actual length of the patch is 9.564.

3.1.5 Calculation of GP dimension

The calculation of GP dimension is obtained by using equation (6) and (7).

$$L_{ground} = 6h + L = 6(1.6) + 9.8128 = 19.413 \text{ mm} \quad (6)$$

$$W_{ground} = 6h + L = 6(1.6) + 13.33 = 22.93 \text{ mm} \quad (7)$$

Several number of feeding methods are presented for microstrip patch antenna, from these, the strip line feed is chosen for this proposed microstrip antenna to obtain the better matching impedance of 50Ω . By using equations (8) and (9) the feeding points are chosen.

$$X_f = \frac{L}{2} \sqrt{\epsilon_{eff}} \quad (8)$$

$$Y_f = \frac{\text{Width of the patch}}{2} \quad (9)$$

The rectangular shape is most commonly employed configuration for the patch antenna due to easy to analysis employing transmission line. This proposed MPA-DGP-UWB antenna is realized by etching rectangular slots in GP of the antenna and allows to obtain the desired frequency bands with better performance.

3.2. Design of the Microstrip Antenna Using HFSS for FR4

The performance of the proposed microstrip patch antenna is analyzed by HFSS software. The design of the antenna follows four types of the setup such as planar EM design, model, excitation and analysis. By utilizing this HFSS software, this research will compute the gain, VSWR, return loss with no error. The proposed antenna design obtained appropriate results using this software when compared with other software.

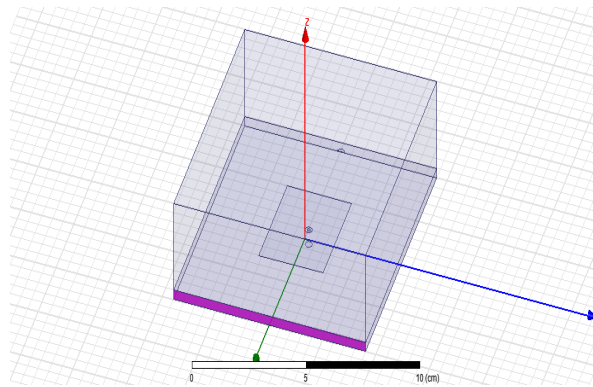


Figure. 5 Design of microstrip antenna in the excitation setup

Creation of infinite conductivity is involved in this proposed microstrip antenna. The position values are evaluated by using the formulas for width, length. In this research, the material for the substrate is taken as Fr4_epoxy which has dielectric constant as 4.4. Fig. 5 shows the design of microstrip antenna in the excitation setup. In excitation setup, draw the lumped port from the assign boundaries window and also create radiation box around the antenna for radiation. The radiation box also has some positions which can be computed. The directivity and the impedance matching can be calculated in this analysis setup. In this analysis setup, return loss, VSWR, and input impedance of proposed microstrip patch antenna are calculated. This can be created by analyzing in far-field report and rectangular plot. Fig. 6 shows the designed microstrip antenna.

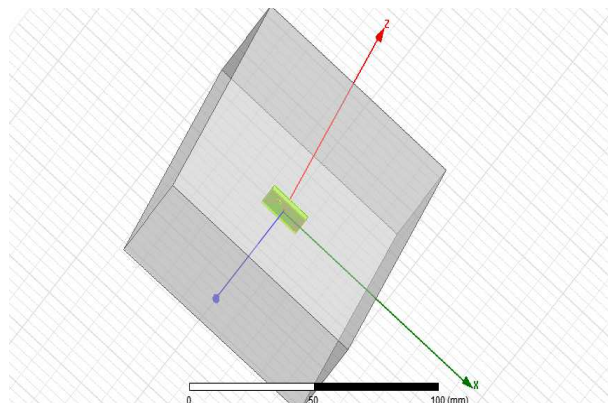


Figure. 6 Designed microstrip antenna

4. Results and discussion

The accomplishment of the prototype of the investigated antenna allows to analyse the performance of the structure and to validate the results are achieved from a simulation. In this research, the DGS antenna is fabricated by utilizing photolithographic approach and chemical etching process by undesirable metal areas of the metallic layer are removed, so that intended design is obtained. Furthermore, the return loss is analysed by Vector Network Analyzer (VNA) PNA-X from Agilent technologies. Here, 50 ohms used for matched impedance with a characteristic impedance. The final reduction dimensions of the microstrip antenna is represented in Table 1.

Table.1 Dimension of the proposed microstrip slot antenna

Parameter	Dimensions (mm)
W_p	13.33
L_p	9.8128
W_{sub}	23
L_{sub}	19
W_{ground}	22.93
L_{ground}	19.413
h	1.6

In this scenario, the simulation and analysis are done for the MPA-DGP-UWB antenna and designed by HFSS simulator.

4.1 Performance Metrics

The performance measures which were obtained from this simulation are given as follows:

4.1.1 Return loss (dB)

It is loss of the power in the signal reflected by a discontinuity in a transmission line. The return loss (dB) is expressed by equation (10).

$$Return Loss (dB) = 10 \log_{10} \frac{P_i}{P_r} \quad (10)$$

Here, P_i is the incident power and P_r is the reflected power. The return loss is related to both reflection coefficient and Standing Wave Ration (SWR).

4.1.2 VSWR

The VSWR is the sum of mismatch between antennas and the feeding line. It is calculated for knowing the amount of reflected power and the mathematical expression for VSWR is given in equation (11).

$$VSWR = \frac{1+|\Gamma|}{1-|\Gamma|} \quad (11)$$

Here, Γ is the Reflection Coefficient (RC).

4.2 Simulation and fabricated results

Figs. 7 and 8 shows the schematic diagram of GP length and return loss plot, it is evident that the antenna provided better bandwidth, and double of bandwidth compared to other existing antennas. The performance VSWR is examined for microstrip antenna and the corresponding plot for the same was obtained. From the simulation result, the VSWR(<2db)is obtained from 9.3Ghz-9.7Ghz frequency range. Figs. 9 shows VSWR performance for GP length of 4 mm and GP width of 11 mm.

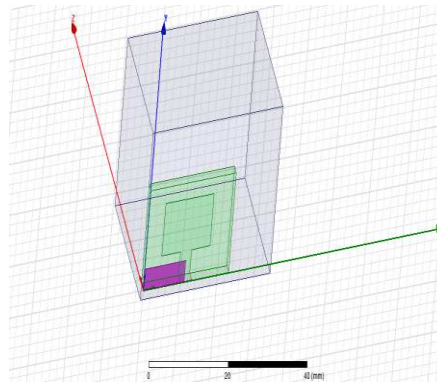


Figure. 7 Schematic diagram of iteration for GP length is 4 mm and GP width is 11 mm

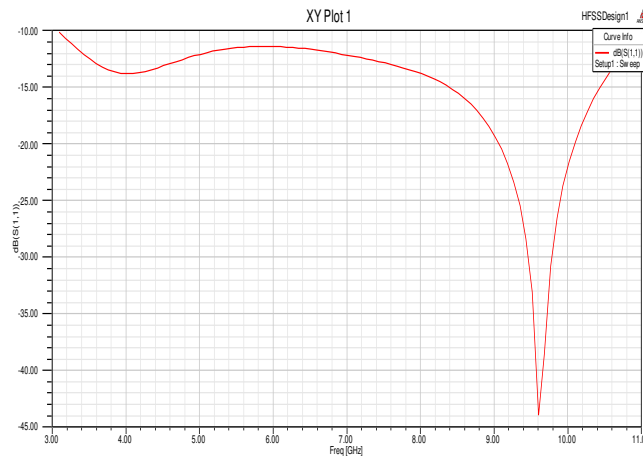


Figure. 8 Return Loss Plot for GP length is 4 mm and the GP width is 11 mm

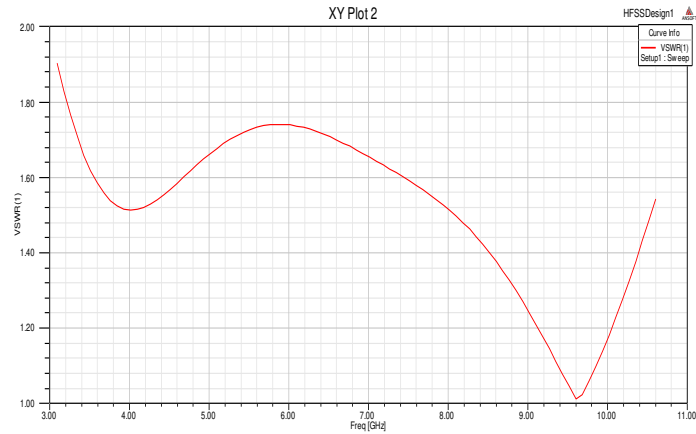


Figure. 9 VSWR Plot for GP length is 4 mm and the GP width is 11 mm

Table.2 shows the iteration of UWB microstrip antenna design using FR4. In the table.2, the number of iteration is 17. There are different parameter dimensions are available such as GP width, GP width, dielectric substrate width, dielectric substrate length, dielectric substrate hight, patch length, patch width, Radiation box width, Radiation box length, and Radiation box hight. The manuscript antenna was obtained better results in the 16th iteration. Table.3 shows the Iteration of UWB microstrip antenna design using FR4. The VSWR and return loss are analysed for 17 iteration. Although the better VSWR and return loss obtained in 16th iteration.

Table. 2 Iteration of UWB microstrip antenna design using FR4

Parameter Dimension (mm)	Number of Iterations																
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
GP width	23	23	20	20	20	20	20	20	20	15	18	23	20	15	13	11	10
GP length	15	10	8	7	12	14	13	11	11	11	11	11	5	4	4	4	4
Dielectric substrate width	23	23	23	23	23	23	23	23	23	23	23	23	23	23	23	23	23
Dielectric substrate length	19	19	19	19	19	19	19	19	19	19	19	19	19	19	19	19	19
Dielectric substrate hight	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6	1.6
Patch width	13	13	13	13	13	13	13	13	13	13	13	13	13	13	13	13	13
Patch length	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10
Radiation box width	35	35	35	35	35	35	35	35	35	35	35	35	35	35	35	35	35
Radiation box length	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
Radiation box hight	22	22	22	22	22	22	22	22	22	22	22	22	22	22	22	22	22

Table. 3 Iteration of UWB microstrip antenna design using FR4

Parameter (GHz)	Number of Iterations																
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Return loss (<-10dB)	10 - 10.1	9.3 - 9.6	8.6 - 10.6	10.2 - 10.6	10.2 - 10.6	7.2- 7.5 & 9.4- 10	9.5- 7.5	9.5- 9.8	7.8- 8.9	8- 10.6	7.9- 8.8 & 9.7- 10.6	7.6- 8.5 & 9.6- 10.6	9.4- 10.6	4- 10.6	3.5- 10.6	3.1- 10.6	3.9-9
VSWR (<2)	10-10.1	9.3- 9.6	8.6- 10.6	10.2- 10.6	10.2- 10.6	7.2- 7.5 & 9.4- 10	9.5- 7.5	9.5- 9.8	7.8- 8.9	8- 10.6	7.9- 8.8 & 9.7- 10.6	7.6- 8.5 & 9.6- 10.6	9.4- 10.6	4- 10.6	3.5- 10.6	3.1- 10.6	3.9-9

Figure. 10 shows the simulated return loss for proposed microstrip slot antenna based on iterations. It indicates the return loss value < -10 dB for the frequency range 3.1 to 10.6 GHz. Further, it proves an increasing value of the return loss from 10.6 GHz onwards. The return loss values for different frequencies 3.1 GHz, 4 GHz, 6 GHz, 8 GHz, 10 GHz are -10 dB, -20.6 dB, -20.4 dB, -20.1 dB, -35 dB. From this obtained results the proposed microstrip slot antenna shows better-simulated return loss about -10 dB. Fig. 11 shows the VSWR value lesser than 2 for the frequency range of 3.1 to 10.6 GHz. At the frequency range 3.1 GHz, the VSWR value is 38. After that, the VSWR values are further decreasing to 25, 25, and 10 for 4 GHz, 5 GHz and 6 GHz, the corresponding VSWR values are 10, 20, 25 and 38. Desired VSWR (< 2) is obtained in the frequency range of 3.1 to 10.6 GHz. It covers the lower and middle range of the UWB antennas. Table.4 shows the size of antenna analysis of proposed multiscrypt antenna with conventional antennas.

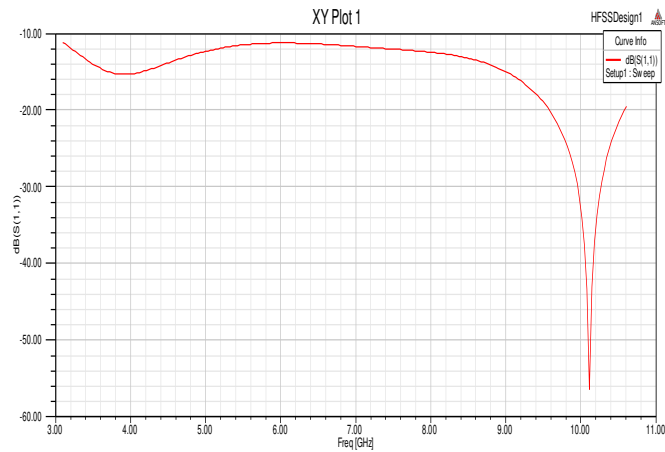


Figure. 10 Simulated graph of return loss

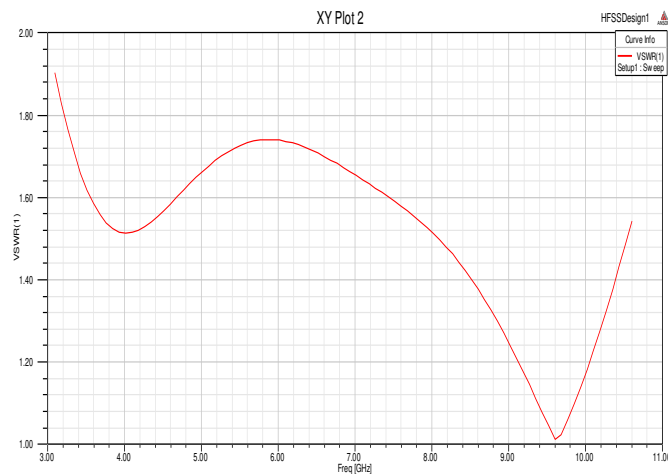


Figure. 11 Simulated graph of VSWR

The simulation results show that the obtained return loss is less than -10dB. Approximately, the proposed antenna obtained -10 dB in 3.1 to 10.6 GHz in UWB band. Finally, this microstrip antenna attained better return loss, which is utilized in UWB applications. This antenna obtains VSWR less than 2 for each UWB band and 6.85 GHz of operating frequency. Figs. 12 and 13 shows the top and back view of the fabricated antenna's tested diagram. Table.4 shows the measurement of the fabricated antenna for testing with the different frequency range.

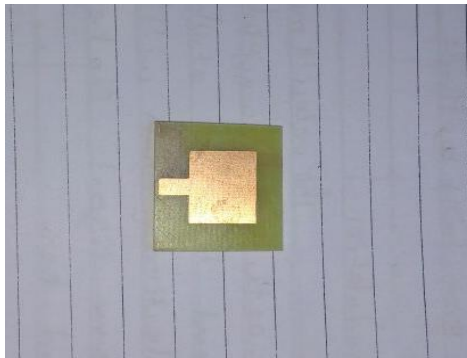


Figure. 12 Fabricated prototype of antenna top view



Figure. 13 Fabricated prototype of antenna back view

Table. 4 Measurement of Fabricated antenna for testing

Frequency (GHz)	Return Loss (dB)
3.6	-9.06
4	-10.1
4.5	-10.6
5	-10.12
5.5	-10.28
6	-10.42
6.5	-10.67
7	-10.97
7.5	-10.84
8	-10.26
8.5	-10.6
9	-10.7
9.5	-10.16
10	-10.13

10.5	-10.1
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The main intention of using DGS is to satisfy the return loss of -10 GHz. The return loss of the original microstrip antenna was enhanced significantly from 3.1 GHz to 10.6 GHz with DGS (Table 5). After optimization process, the size of original microstrip antenna has reduced compared to the existing microstrip antenna size.

Table. 5 Size analysis of proposed multiscrypt antenna with conventional antennas

Author	Antenna Size mm	Frequency (GHz)	Return Loss (dB)	VSWR
Pandhare [14]	23×24×0.51	6.22	39.53	-
Tiwari et al. [15]	25×17×1.6	2.94	5	≤ 2
Sharma, Narinder, and Vipul Sharma [16]	45×38.92×1.6	2.41 & 8.34	-14.86 & -15.76	1.44 & 1.39
	45×38.92×1.6	2.68 & 8.38	-14.26 & 16.11	1.48 & 1.37
Proposed microstrip antenna	10×13×1.6	3.1 to 10.6	-10	<2

4.3 Network Analyzer

The VNA is a one of the efficient network computing tool that can be utilized to compute the input impedance as a frequency function. Otherwise, it can plot both S11 (return loss) and VSWR based on frequency-dependent function of the antenna impedance. The VNA is shown fig. 13.



Figure. 13 Vector Network Analyzer

The VNA transfers a very small quantity of power to the proposed antenna and the reflected power is calculated by using VNA Tool. The S – parameter is the fundamental magnitude of reflection coefficient, it depends on antenna impedance and impedance of VNA that is typically 50 Ω . The points above the equator of the Smith chart denotes an inductive impedance with a positive reactance. As well as points under the equator of the Smith chart denote capacitive impedance with a negative reactance. The Smith chart is an effective tool of viewing impedance over a frequency range in a concise, and clear form.

Comparison between reported and proposed UWB Antennas:

Here, this table shows the comparison of the reported and proposed UWB antennas. It is showing the proposed antenna have comparatively achieved 3.1 to 10.6 GHz throughout the band.

Table. 6 Comparison between reported and proposed UWB Antennas

S. No	Title & Ref No.	Shape	Size (mm ²)	Material	Thickness (mm)	Operating Frequency (GHz)
1.	Bandwidth Improvement of UWB Microstrip Antenna Using Finite Ground Plane [17]	Rectangle Patch	12x16	FR-4	1.6	6.5 to 13.7
2.	Microstrip Antenna Design for UWB Applications [18]	Circular Patch	31.17x40	FR-4	0.787	2.7 to 8.2
3.	Novel Microstrip Antenna Aims at UWB Applications [19]	Rectangle Patch	20x15	Rogers RO4003	0.813	2.65 to 10
4.	Design and construction of microstrip UWB antenna with time domain analysis [20]	Rectangle Patch	18x11	FR-4	1.6	4.1 to 10
5.	<i>Proposed Antenna</i>	<i>Rectangle Patch</i>	<i>10x13</i>	<i>FR-4</i>	<i>1.6</i>	<i>3.1 to 10.6</i>

5. Conclusion

In this research, a compact multiband and miniature microstrip antenna for UWB application is presented. This proposed antenna is designed based on a simple DGS through etching slots on GP, so it can be much easier to fabricate. The measured results shows that the obtained return loss is -10 dB and VSWR is lesser than 2 at 3.1- 10.6 GHz for better multiband band UWB applications. The features of the proposed microstrip slot antenna have been analyzed through various parametric studies using HFSS simulation software. The proposed antenna design can be utilized for UWB applications in the frequency range 3.1 GHz to 10.6 GHz, which is covering the bandwidth of lower and middle-frequency bands. The simulation results prove that the obtained return loss, VSWR and bandwidth is better for UWB applications for using FR4. In future work, the slot-line is considered in the microstrip design for band rejection or band enhancement to reduce the antenna size.

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Conflicts of interest/Competing interests

I have not Conflict of Interest.

Availability of data and material

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Code Availability

Not Applicable

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